

In re application of: Choi, et al.

Serial No.: 10/083,261

Examiner: George Y. Wang

Filed: February 25, 2002

Group Art Unit: 2871

For: **THIN FILM TRANSISTOR ARRAY SUBSTRATE USING LOW DIELECTRIC INSULATING LAYER AND METHOD OF FABRICATING THE SAME**

Mail Stop RCE
Commissioner for Patents
PO Box 1450
Alexandria VA 22313-1450

AMENDMENT TRANSMITTAL FORM

Sir:

Transmitted herewith is an amendment in the above-identified application.

- ☐ Small entity status of this application under 37 C.F.R. § 1.9 and 1.27 has been established by a verified statement previously submitted.
☐ A verified statement to establish small entity under 37 C.F.R. § 1.9 and 1.27 is enclosed.
☒ No additional fee is required.

The fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	(Col. 3)	SMALL ENTITY	OTHER THAN SMALL ENTITY
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDIT. FEE OR RATE ADDIT. FEE
TOTAL	32 *	MINUS 32**	= 0	X 9	\$ 0 X 18 \$ 0
INDEP.	4*	MINUS 5***	= 0	X 44	\$ 0 X 88 \$ 0
[] FIRST PRESENTATION OF MULTIPLE DEP. CLAIM				X 150	\$ X 300 \$ 0
TOTAL				ADDIT. FEE \$ 0	OR TOTAL \$ 0

* If the entry in Co. 1 is less than entry in Col. 2, write "0" in Col. 3.

** If the "Highest No. Previously Paid for" IN THIS SPACE is less than 20, enter "20".


*** If the "Highest No. Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The Highest No. Previously Paid For" (Total or indep.) is the highest number found in the appropriate box in Col. 1 of a prior amendment or the number of claims originally filed.

- ☐ Please charge Deposit Account No. _____ in the amount of \$ _____. Two (2) copies of this sheet are enclosed.
☐ Please charge fee of \$ ____ for _____ by Credit Card Payment Form PTO-2038 enclosed herewith
☒ Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. § 1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account No. 50-0679. Also, in the event any extensions of time for responding are required for the pending application(s), please treat this paper as a petition to extend the time as required and charge Deposit Account No. 50-0679 therefor. **TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.**

Respectfully submitted,

F. CHAU & ASSOCIATES
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Woodbury New York 11797
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David L. Heath
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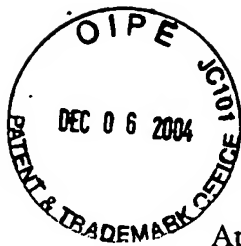
CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Mail Stop RCE, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on

December 3, 2004.

Dated: 12/3/04


David L. Heath



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Choi, et al. Examiner: George Y. Wang
Serial No: 10/083,261 Group Art Unit: 2871
Filed: February 25, 2002 Docket: 8071-12 (OPP 011059US)
For: **THIN FILM TRANSISTOR ARRAY SUBSTRATE USING
LOW DIELECTRIC INSULATING LAYER AND METHOD
OF FABRICATING THE SAME**

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

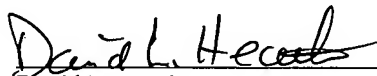
Sir:

Prior to examination of the above-referenced application, please amend the claims as follows. No new matter is introduced.

CERTIFICATE OF MAILING 37 C.F.R. §1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on **December 3, 2004**.

Dated: 12/3/04


David L. Heath